

# EVVOSEMI<sup>®</sup>

THINK CHANGE DO



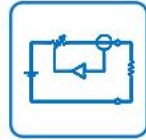
ESD



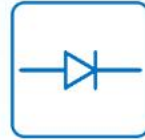
TVS



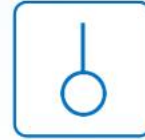
MOS



LDO



Diode



Sensor



DC-DC

## Product Specification

▶ Domestic	Part Number	IRLU120N
▶ Overseas	Part Number	IRLU120N
▶ Equivalent	Part Number	IRLU120N

EV is the abbreviation of name EVVO

## N-Ch 100V Fast Switching MOSFETs

- ★ Advanced high cell density Trench technology
- ★ Super Low Gate Charge
- ★ Excellent Cdv/dt effect decline
- ★ Green Device Available

### Product Summary

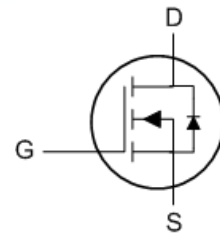
BVDSS	R <sub>DS(on)</sub>	I <sub>D</sub>
100V	152mΩ	9 A

### Description

The IRLU120N is the high cell density trenched N-ch MOSFETs, which provide excellent R<sub>DS(on)</sub> and gate charge for most of the synchronous buck converter applications.

The IRLU120 meet the RoHS and Green Product requirement with full function reliability approved.

### TO251 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	9	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	5.7	A
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	2.3	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	1.8	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	18	A
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	31	W
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>3</sup>	2	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	---	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	4	°C/W

**N-Ch 100V Fast Switching MOSFETs**
**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.122	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	---	---	152	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	---	---	158	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-4.84	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	10	uA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	100	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =8A	---	10.2	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	2.3	---	Ω
Q <sub>g</sub>	Total Gate Charge (10V)	V <sub>DS</sub> =60V, V <sub>GS</sub> =10V, I <sub>D</sub> =8A	---	25.5	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	4.2	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	4.3	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω I <sub>D</sub> =1A	---	17.3	---	ns
T <sub>r</sub>	Rise Time		---	2.8	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	50	---	
T <sub>f</sub>	Fall Time		---	2.8	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	1077	---	pF
C <sub>oss</sub>	Output Capacitance		---	46	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	32	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	9	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>		---	---	18	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V

Note :

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

N-Ch 100V Fast Switching MOSFETs

Typical Characteristics

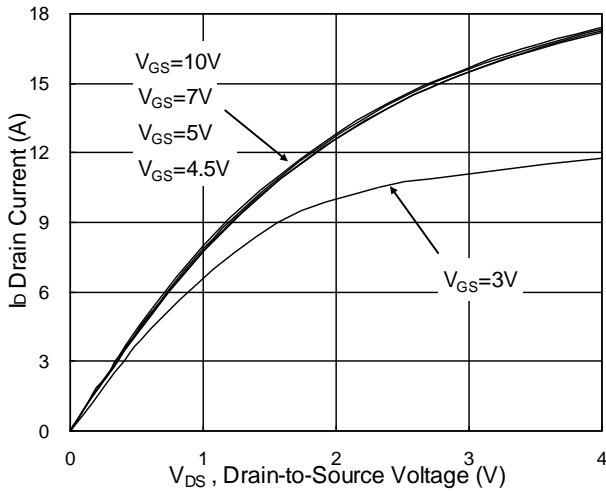


Fig.1 Typical Output Characteristics

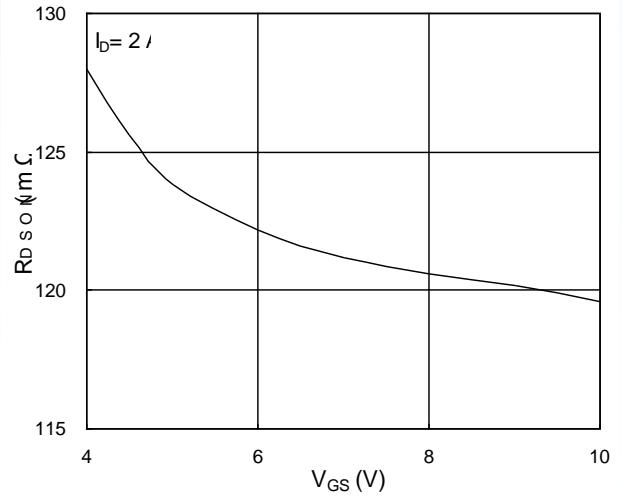


Fig.2 On-Resistance vs. Gate-Source

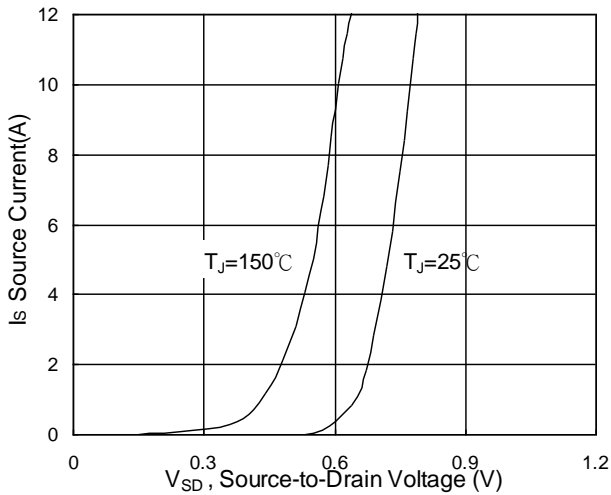


Fig.3 Forward Characteristics Of Reverse

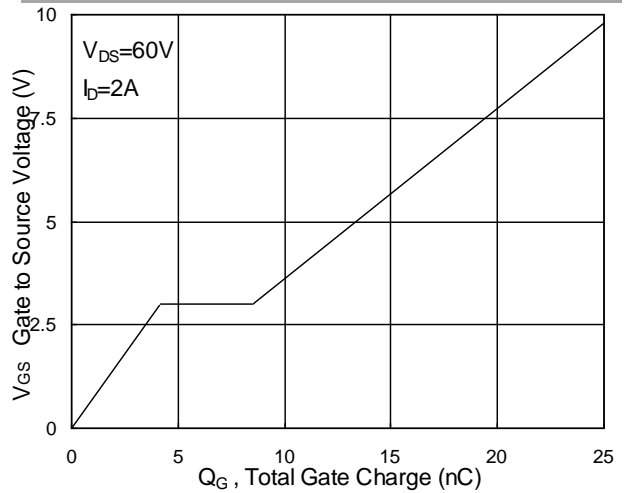


Fig.4 Gate-Charge Characteristics

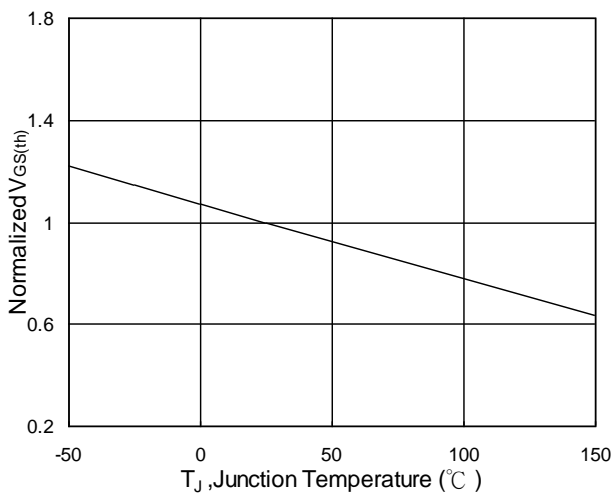


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

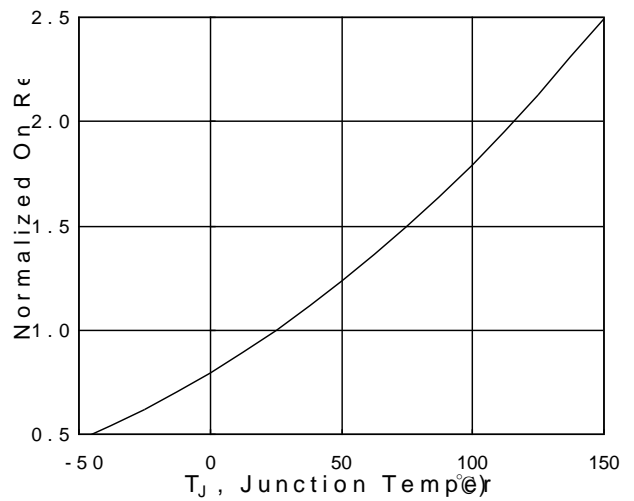


Fig.6 Normalized R<sub>DS(on)</sub> vs. T<sub>J</sub>

N-Ch 100V Fast Switching MOSFETs

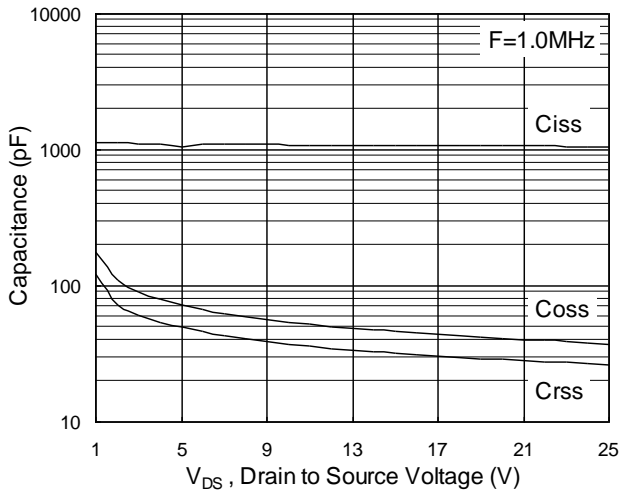


Fig.7 Capacitance

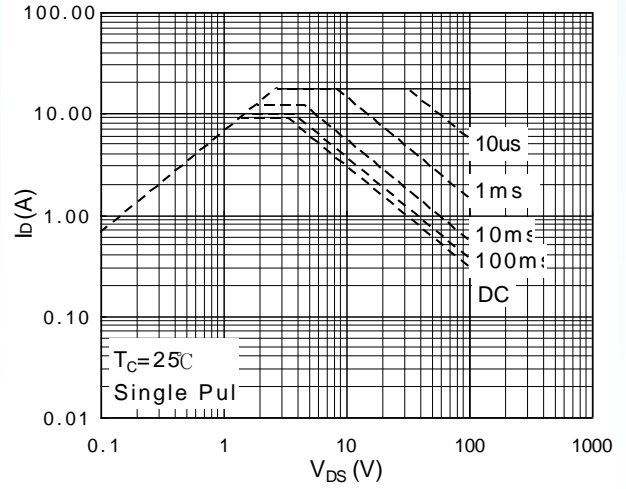


Fig.8 Safe Operating Area

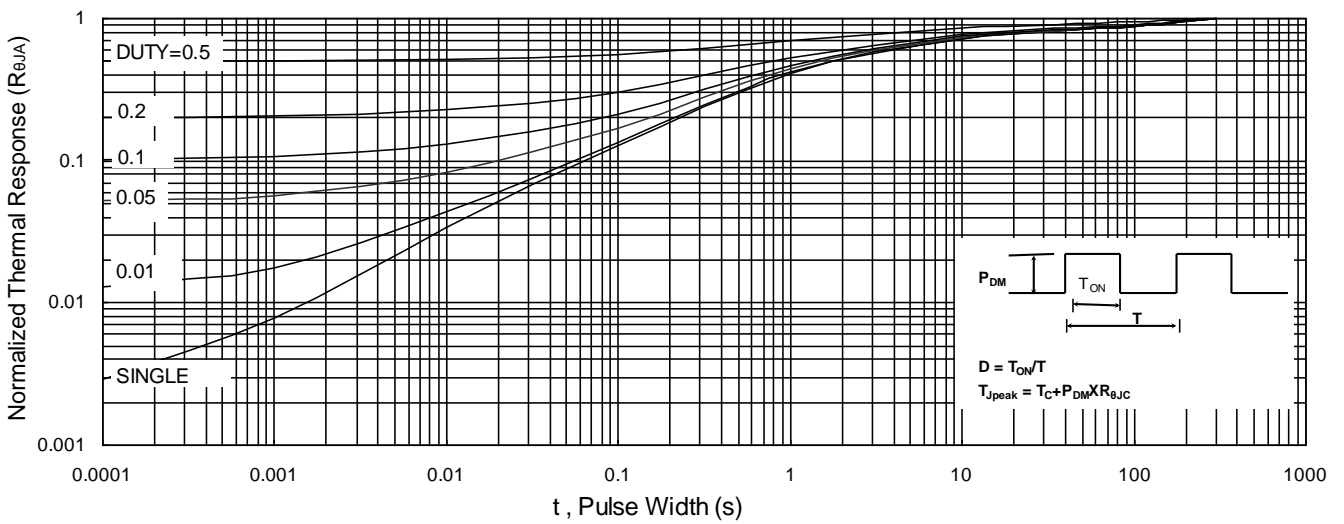


Fig.9 Normalized Maximum Transient Thermal Impedance

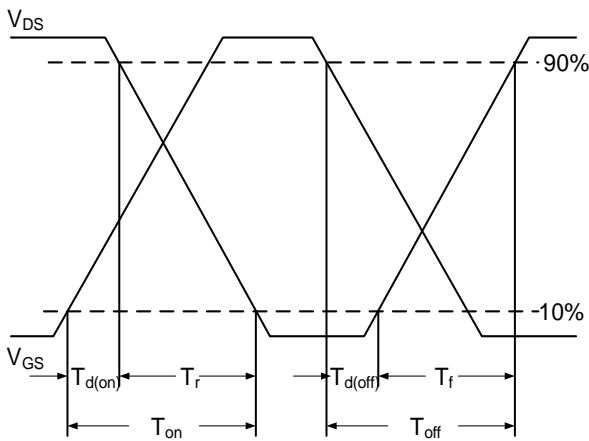


Fig.10 Switching Time Waveform

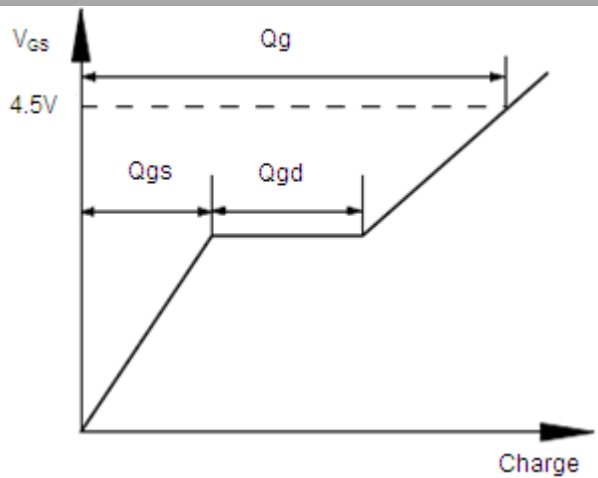


Fig.11 Gate Charge Waveform

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