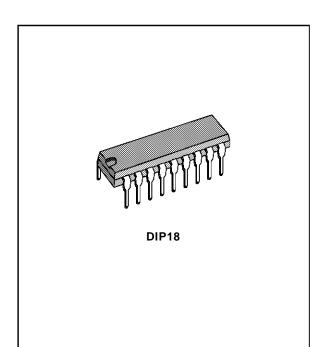


## ULN2801A ULN2802A - ULN2803A ULN2804A - ULN2805A

# **EIGHT DARLINGTON ARRAYS**

- EIGHT DARLINGTONS WITH COMMON EMIT-TERS
- OUTPUT CURRENT TO 500 mA
- OUTPUT VOLTAGE TO 50 V
- INTEGRAL SUPPRESSION DIODES
- VERSIONS FOR ALL POPULAR LOGIC FAMI-LIES
- OUTPUT CAN BE PARALLELED
- INPUTS PINNED OPPOSITE OUTPUTS TO SIMPLIFY BOARD LAYOUT



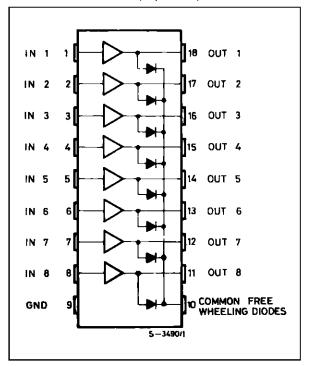
#### **PIN CONNECTION** (top view)

#### DESCRIPTION

The ULN2801A-ULN2805A each contain eight darlington transistors with common emitters and integral suppression diodes for inductive loads. Each darlington features a peak load current rating of 600mA (500mA continuous) and can withstand at least 50V in the off state. Outputs may be paralleled for higher current capability.

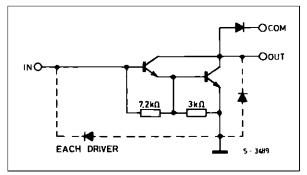
Five versions are available to simplify interfacing to standard logic families : the ULN2801A is designed for general purpose applications with a current limit resistor ; the ULN2802A has a 10.5k $\Omega$  input resistor and zener for 14-25V PMOS ; the ULN2803A has a 2.7k $\Omega$  input resistor for 5V TTL and CMOS ; the ULN2804A has a 10.5k $\Omega$  input resistor for 6-15V CMOS and the ULN2805A is designed to sink a minimum of 350mA for standard and Schottky TTL where higher output current is required.

All types are supplied in a 18-lead plastic DIP with a copper lead from and feature the convenient inputopposite-output pinout to simplify board layout.

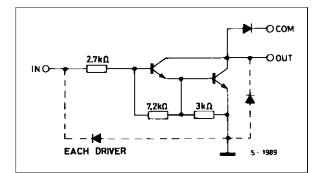


#### SCHEMATIC DIAGRAM AND ORDER CODES

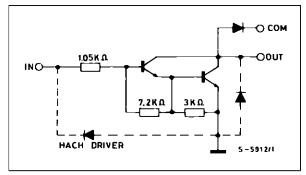
For ULN2801A (each driver for PMOS-CMOS)



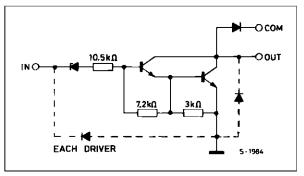
For ULN2803A (each driver for 5 V, TTL/CMOS)



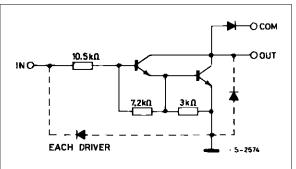
For ULN2805A (each driver for high out TTL)



For ULN2802A (each driver for 14-15 V PMOS)



For ULN2804A (each driver for 6-15 V CMOS/PMOS



#### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vo	Output Voltage	50	V
Vi	Input Voltage for ULN2802A, UL2803A, ULN2804A for ULN2805A	30 15	V
lc	Continuous Collector Current	500	mA
IB	Continuous Base Current	25	mA
P <sub>tot</sub>	Power Dissipation (one Darlington pair) (total package)	1.0 2.25	W
T <sub>amb</sub>	Operating Ambient Temperature Range	– 20 to 85	°C
T <sub>stg</sub>	Storage Temperature Range	– 55 to 150	°C

#### THERMAL DATA

Symbol	Parameter	Value	Unit
R <sub>th j–amb</sub>	Thermal Resistance Junction-ambient Max	55	°C/W

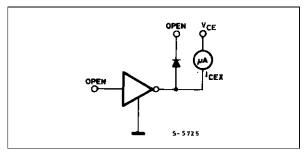
# **ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	Fig.
ICEX	Output Leakage Current	$V_{CE} = 50V$ T <sub>amb</sub> = 70°C, V <sub>CE</sub> = 50V			50 100	μΑ μΑ	1a 1a
		$ \begin{array}{l} T_{amb} = 70^{\circ}C \\ for \ ULN2802A \\ V_{CE} = 50V, \ V_i = 6V \\ for \ ULN2804A \\ V_{CE} = 50V, \ V_i = 1V \end{array} $			500 500	μA μA	1b 1b
V <sub>CE(sat)</sub>	Collector-emitter Saturation Voltage	$\begin{array}{ll} I_{C} = 100 \text{mA}, \ I_{B} = 250 \mu \text{A} \\ I_{C} = 200 \text{mA}, \ I_{B} = 350 \mu \text{A} \\ I_{C} = 350 \text{mA}, \ I_{B} = 500 \mu \text{A} \end{array}$		0.9 1.1 1.3	1.1 1.3 1.6	V V V	2
l <sub>i(on)</sub>	Input Current	$ \begin{array}{llllllllllllllllllllllllllllllllllll$		0.82 0.93 0.35 1 1.5	1.25 1.35 0.5 1.45 2.4	mA mA mA mA mA	3
I <sub>i(off)</sub>	Input Current	$T_{amb} = 70^{\circ}C, I_{C} = 500\mu A$	50	65		μA	4
Vi(on)	Input Voltage	$\begin{array}{c} V_{CE} = 2 \ V \\ \text{for } ULN2802A \\ I_{C} = 300\text{mA} \\ \text{for } ULN2803A \\ I_{C} = 200\text{mA} \\ I_{C} = 250\text{mA} \\ I_{C} = 300\text{mA} \\ \text{for } ULN2804A \\ I_{C} = 125\text{mA} \\ I_{C} = 200\text{mA} \\ I_{C} = 275\text{mA} \\ I_{C} = 350\text{mA} \\ \text{for } ULN2805A \\ I_{C} = 350\text{mA} \\ \text{for } ULN2805A \\ I_{C} = 350\text{mA} \\ \end{array}$			13 2.4 2.7 3 5 6 7 8 2.4	V V V V V V V V V	5
h <sub>FE</sub>	DC Forward Current Gain	for ULN2801A $V_{CE} = 2V, I_{C} = 350mA$	1000			_	2
Ci	Input Capacitance			15	25	pF	-
t <sub>PLH</sub>	Turn-on Delay Time	0.5 V <sub>i</sub> to 0.5 V <sub>o</sub>		0.25	1	μs	-
t <sub>PHL</sub>	Turn-off Delay Time	0.5 $V_i$ to 0.5 $V_o$		0.25	1	μs	-
I <sub>R</sub>	Clamp Diode Leakage Current				50 100	μΑ μΑ	6 6
VF	Clamp Diode Forward Voltage	I <sub>F</sub> = 350mA		1.7	2	V	7

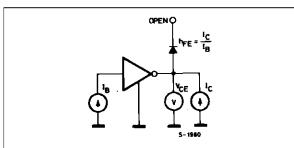


## **TEST CIRCUITS**

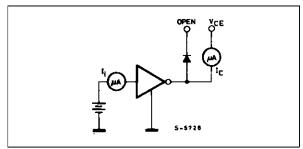
# Figure 1a.



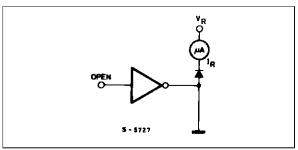
# Figure 2.



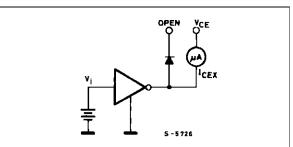
## Figure 4.



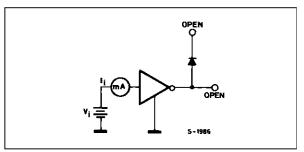
# Figure 6.



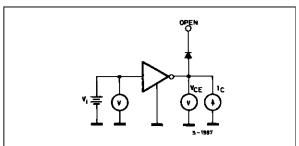














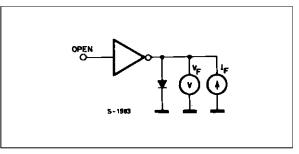




Figure 8 : Collector Current as a Function of Saturation Voltage.

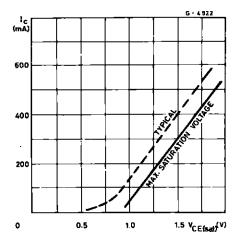


Figure 10 : Allowable Average Power Dissipation as a Function of Ambient Temperature.

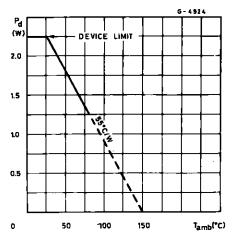


Figure 12 : Peak Collector Current as a Function of Duty.

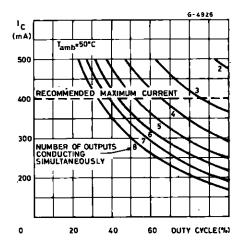


Figure 9 : Collector Current as a Function of Input Current.

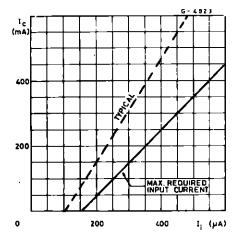


Figure 11 : Peak Collector Current as a Function of Duty Cycle.

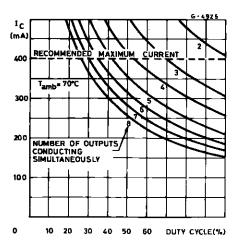
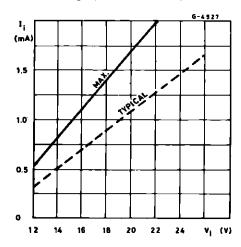


Figure 13 : Input Current as a Function of Input Voltage (for ULN2802A).



## ULN2801A - ULN2802A - ULN2803A - ULN2804A - ULN2805A

Figure 14 : Input Current as a Function of Input Voltage (for ULN2804A)

Figure 15 : Input Current as a Function of Input Voltage (for ULN2803A)

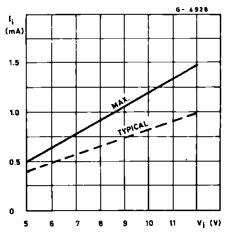
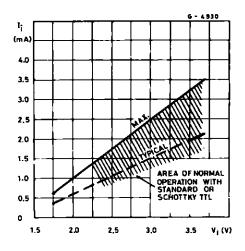
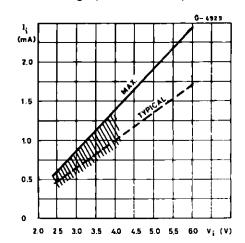


Figure 16 : Input Current as a Function of Input Voltage (for ULN2805A)

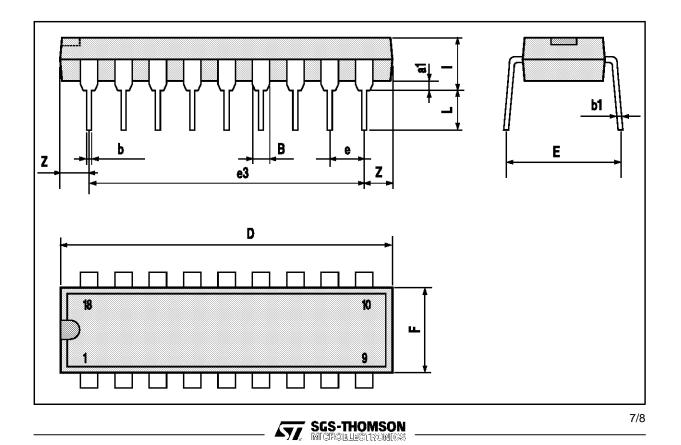






## **DIP18 PACKAGE MECHANICAL DATA**

DIM.		mm			inch	
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.254			0.010		
В	1.39		1.65	0.055		0.065
b		0.46			0.018	
b1		0.25			0.010	
D			23.24			0.915
E		8.5			0.335	
е		2.54			0.100	
e3		20.32			0.800	
F			7.1			0.280
I			3.93			0.155
L		3.3			0.130	
Z		1.27	1.59		0.050	0.063



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