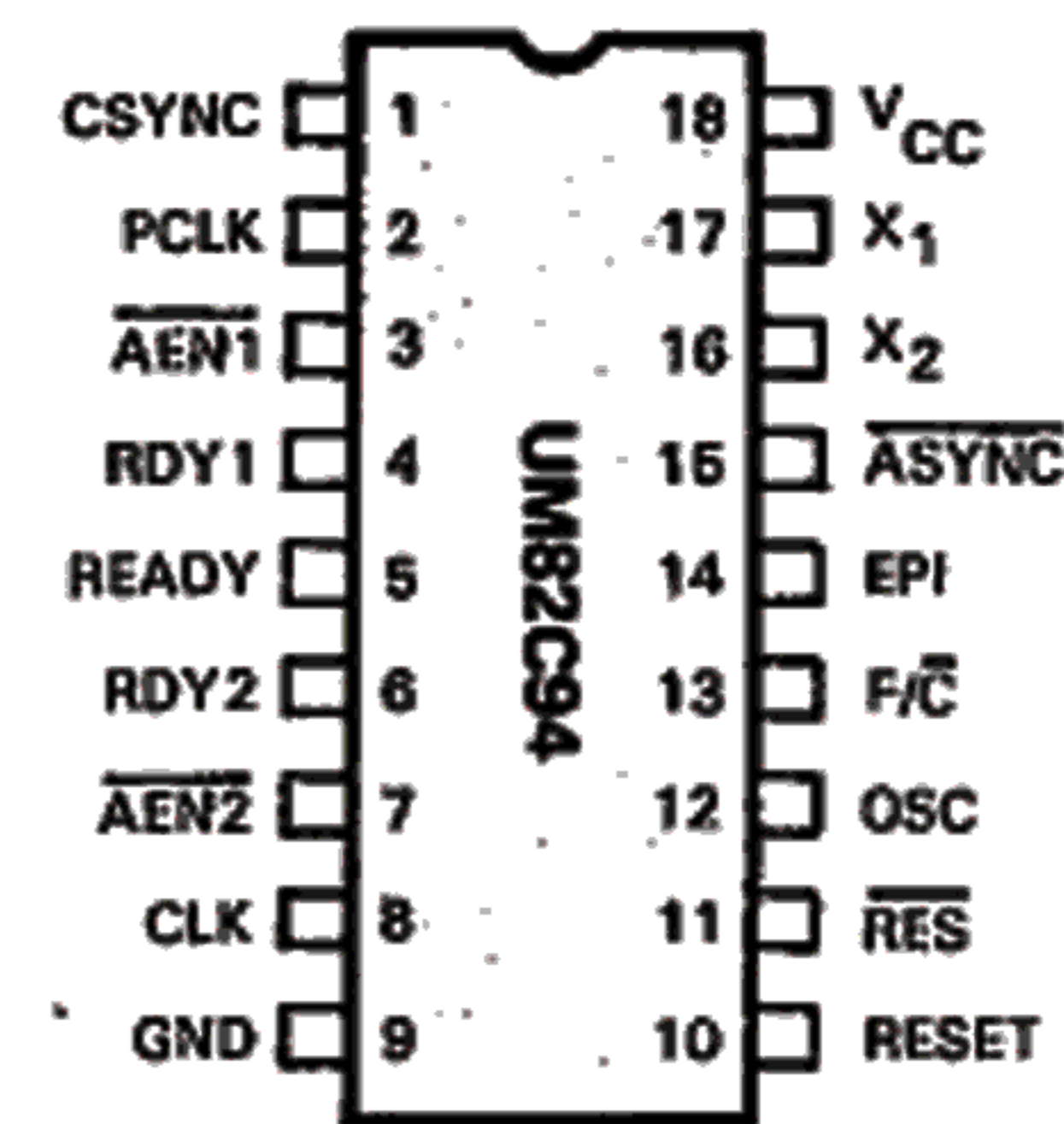


UM82C94 CMOS Clock Generator & Driver

FEATURES

- Generates the System Clock for CMOS or NMOS Microprocessors.
- Up to 25 MHz Operation.
- Uses a Parallel Mode Crystal Circuit or External Frequency Source.
- Provides Ready Synchronization.
- Generates System Reset Output from Schmitt Trigger Input.
- Capable of Clock Synchronization with Other 82094
- TTL Compatible Inputs/Outputs.
- Very Low Power Consumption.
- 18-pin Package.
- Single +5V Power Supply.
- Required 2 Times the Frequency of the Desired CLK Output

PIN CONFIGURATION



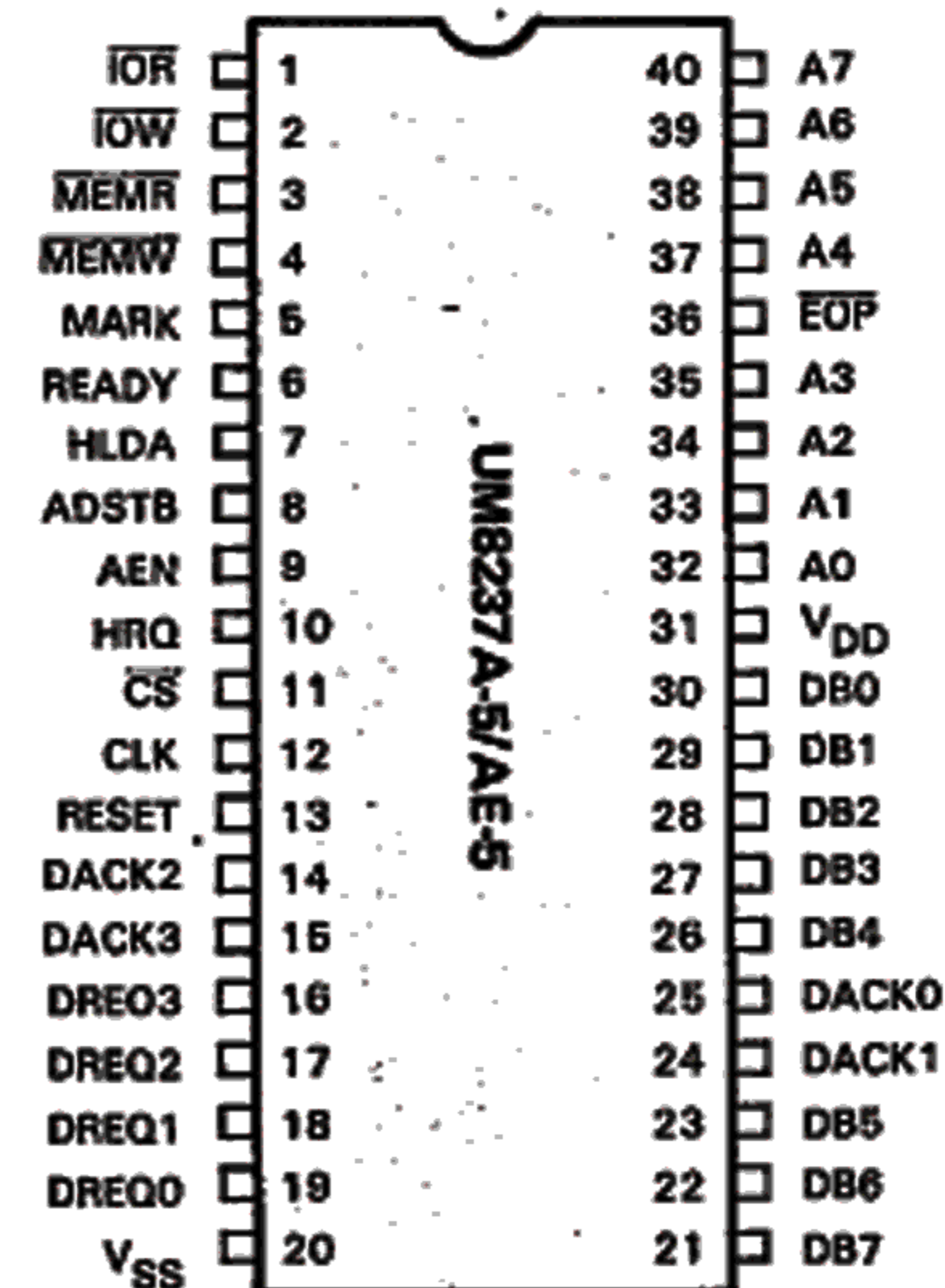
CONTROL PIN	LOGIC 1	LOGIC 0
F/C	External Clock	Crystal Drive
RES	Normal	Reset
RDY1 RDY2	Bus Ready	Bus Not Ready
AEN1 AEN2	Address Disabled	Address Enabled
ASYNC	2 Stage Ready Synchronization	1 Stage Ready Synchronization

UM8237A-5/AE-5 Programmable DMA Controller

FEATURES

- Enable/Disable Control of Individual DMA Requests
- Four Independent DMA Channels
- Independent Autoinitialization of all Channels
- Memory-to-memory Transfers
- Memory Block Initialization
- Address Increment or Decrement
- High Performance: Transfers up to 1.6M Bytes/Second with 5 MHz 8237A-5
- Directly Expandable to any Number of Channels
- End of Process Input for Terminating Transfers
- Software DMA Requests
- Independent Polarity Control for DREQ and DACK Signals
- Available in EXPRESS
— Standard Temperature Range

PIN CONFIGURATION



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Datasheets for electronic components.