MBRS140T3G, SBRS8140T3G

Surface Mount Schottky Power Rectifier

Schottky Power Rectifiers employ the use of the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system.

Features

- Small Compact Surface Mountable Package with J-Bend Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop (0.55 V Max @ 1.0 A, T_J = 25°C)
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guardring for Stress Protection
- ESD Ratings:
 - Human Body Model = 3B (> 8000 V)
 - Machine Model = C (> 400 V)
- SBRS8 Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- All Packages are Pb-Free*

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band



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SCHOTTKY BARRIER RECTIFIER 1.0 AMPERE, 40 VOLTS



SMB CASE 403A

MARKING DIAGRAM



B14	= Device Code
Α	= Assembly Location
Y	= Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]	
MBRS140T3G	SMB (Pb-Free)	2,500 / Tape & Reel	
SBRS8140T3G	SMB (Pb-Free)	2,500 / Tape & Reel	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Semiconductor Components Industries, LLC, 2014 June, 2014 – Rev. 10

MBRS140T3G, SBRS8140T3G

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current $T_L = 115^{\circ}C$	I _{F(AV)}	1.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	40	A
Operating Junction Temperature	TJ	-65 to +125	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

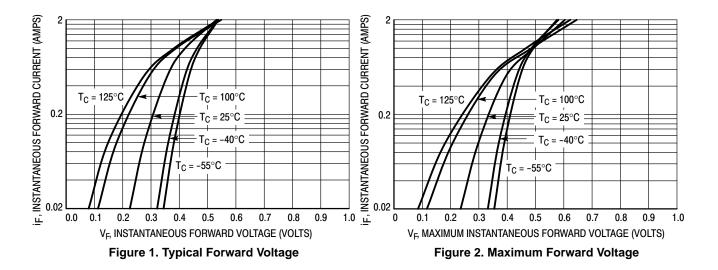
Characteristic		Value	Unit
Thermal Resistance – Junction–to–Lead $(T_L = 25^{\circ}C)$	$R_{ extsf{ heta}JL}$	12	°C/W

ELECTRICAL CHARACTERISTICS

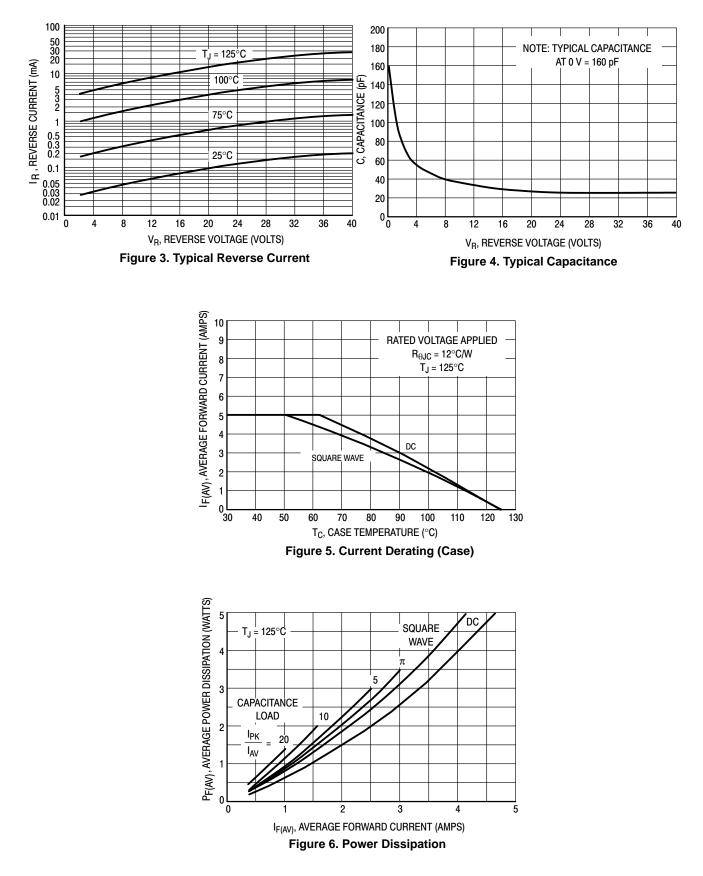
Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C}$)	V _F	0.6	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 100^{\circ}C$)	İR	1.0 10	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.



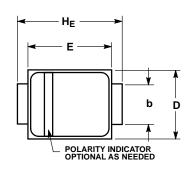
MBRS140T3G, SBRS8140T3G

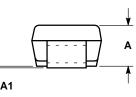


MBRS140T3G, SBRS8140T3G

PACKAGE DIMENSIONS

SMB CASE 403A-03 **ISSUE H**



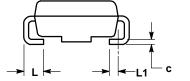


NOTES

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH. 1. 2

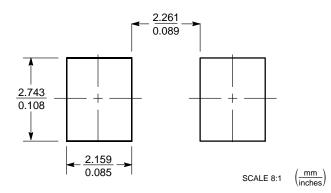
D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P. 3.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.90	2.20	2.28	0.075	0.087	0.090
A1	0.05	0.10	0.19	0.002	0.004	0.007
b	1.96	2.03	2.20	0.077	0.080	0.087
с	0.15	0.23	0.31	0.006	0.009	0.012
D	3.30	3.56	3.95	0.130	0.140	0.156
Е	4.06	4.32	4.60	0.160	0.170	0.181
HE	5.21	5.44	5.60	0.205	0.214	0.220
L	0.76	1.02	1.60	0.030	0.040	0.063
L1	0.51 REF			0.020 REF		





SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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